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SEP 16 2005

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Re:

To: Examiner Fernando L. Toledo From: Les lie S. Szivos

Art Unit: 2823

Fax: 571-273-8300 Pages: 6 pages including fax cover

**Phone: Date:** 9/16/2005

Firding: Bate: 9/10/2003

Hussein I. Hanafi et al.
U.S. Patent Appln. No. 10/659,949

Docket: YOR920020182US2 (15790A)

## **URGENT!!**

Please see the following documents:

- 1. Certificate of transmission by facsimile dated September 16, 2005
- 2. Transmittal letter (in duplicate)
- 3. Authorization to charge IBM deposit account
- 4. Response under 37 C.F.R. §§1.111 and 1.143

If you have any questions concerning this facsimile please do not hesitate to contact Vivian Henriquez at 516-742-4343. Thank you.

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CERTIFICATE OF T Applicant(s): Hussein I. I	TRANSMISSION BY FAC	SIMILE (37 CFR 1.8)	į	ocket No. 0182US2 (15790)
Application No. 10/659,949	Filing Date September 11, 2003	Examiner Fernando L. Toledo		Group Art Unit 2823
	TANCE T-GATE MOSFET DEV ND AN INNOVATIVE OXIDE		E GATE	
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I hereby certify that this	Respons	e under 37 C.F.R. §§1.111 AN	D 1.143	
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on September 11		,		
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	Note: Each paper must ha	ve its own certificate of mailing.		
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	TRANSMIT (General - P	Docket No. YOR920020182US2 (15790A)					
In Re Application Of: Hussein I. Hanafi et al.							
Application No. 10/659,949	Filing Date September 11, 2003	Examiner Fernando L. Toledo	Customer No. 23389	Group Art Unit 2823	Confirmation No. 5877		
Title: LOW RESISTANCE T-GATE MOSFET DEVICE USING A DAMASCENE GATE PROCESS AND AN INNOVATIVE OXIDE REMOVAL ETHCH							
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		COMMISSIONER FOR PAT	ENTS:		·		
Transmitted herew							
Response under :	37 C.F.R. §§1.111 and	1.143					
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in the above ident	ified application.						
No addition	al fee is required.						
☐ A check in the amount of is attached.							
	-	d to charge and credit Deposit	Account Ind.	50-0510/IBM	:		
☐ Cha	arge the amount of						
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	arge any additional fe	· ·					
	credit card. Form PT Information on this	form may become public. C	redit card info	rmation should	not be		
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·			Dated: Sept	ember 16, 2005			
Z 11 0 0 0 1	Signature /	·			·		
Leslie S. Szivos, Ph Reg. No. 39,394	.D.						
SCULLY, SCOTT,	MURPHY & PRESS	ER	1 hereby certify	that this corre	spondence is being		
400 Garden City Pl Garen City, New Y			deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 28313-1450" [37 FR 1.8(a)] on				
			(Date)				
T 00 1			Signatur	e Person Mailing	Correspondence		
LSS:vh cc:							
			Typed or Printe	ed Name of Person M	ailing Correspondence		

TRANSMITTAL LETTER (General - Patent Pending)				Docket No. YOR920020182US2 (15790A)				
In Re Application Of: Hussein I. Hanafi et al.								
Application No. 10/659,949	Filing Date September 11, 2003	Examiner Fernando L. Toledo	Customer No. 23389	Group Art Unit 2823	Confirmation No. 5877			
Title: LOW RESISTANCE T-GATE MOSFET DEVICE USING A DAMASCENE GATE PROCESS AND AN INNOVATIVE OXIDE REMOVAL ETHCH								
- - -		COMMISSIONER FOR PAT	ENTS:					
Transmitted herew Response under	vith is: 37 C.F.R. §§1.111 and	d 1.143						
in the above identified application.  No additional fee is required.  A check in the amount of is attached.  The Director is hereby authorized to charge and credit Deposit Account No. 50-0510/IBM as described below.  Charge the amount of  Credit any overpayment.  Charge any additional fee required.  Payment by credit card. Form PTO-2038 is attached.  WARNING: Information on this form may become public. Credit card Information should not be included on this form. Provide credit card information and authorization on PTO-2038.								
Leslie S. Szivos, Ph Reg. No. 39,394	Signature		Dated: Sept	ember 16, 2005				
		SER	deposited with sufficient pasta addressed to it 1450, Alexandria (Date)	the United State ge as first class "Commissioner a, VA 22313-1450"				
cc:			Typed or Printe	ed Name of Person M	ailing Correspondence			

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#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Hussein I. Hanafi et al.

Examiner: Fernando L. Toledo

Serial No: 10/659,949

Art Unit: 2823

Filed: September 11, 2003

**Docket:** YOR920020182US2 (15790A)

For: LOW RESISTANCE T-GATE MOSFET

Dated: September 16, 2005

DEVICE USING A DAMASCENE GATE PROCESS AND AN INNOVATIVE OXIDE

**REMOVAL ETCH** 

Confirmation No.: 5877

Mail Stop Amendment Commissioner for Patents United States Patent Office Alexandria, VA 23313-1450

### **RESPONSE UNDER 37 C.F.R. §§1.111 AND 1.143**

Sir:

In response to the Office Action dated August 16, 2005, applicants provisionally elect, without traverse, the subject matter of Group II, i.e., Claims 14-20 for continued prosecution in the above-identified patent application.

#### **CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: September 16, 2005

Leslie S. Szivos